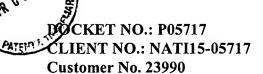
PATENT



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

DARREN LEE RUST, ET AL.

U.S. Serial No.

10/698,007

Filed

October 30, 2003

For

SEMICONDUCTOR WAFER FABRICATION FURNACE IDLE

MONITOR AND METHOD OF OPERATION

Group No.

2812

Examiner

(Not Yet Assigned)

MAIL STOP MISSING PARTS

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

LETTER TO OFFICIAL DRAFTSMAN

Applicant submits four (4) sheets of formal drawings (FIGURES 1-4) in the above-referenced patent application.

Respectfully submitted,

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Date: March 30, 2004

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